Reset your thinking about clocks.
In precision timing, analog is everywhere.

AD9516 14-Channel Clock Generator with Integrated VCO

- Two reference inputs
- Integer-N PLL frequency synthesizer
- Selectable VCO frequencies up to 2.95 GHz
- 14 outputs with total jitter < 0.5 ps rms
- Programmable dividers, up to 1024
- Four adjustable delay lines
- Selectable LVPECL, LVDS/CMOS logic
- 64-lead LFCSP
- 5k price: $11.47

AD9549 Network Clock Generator/Synchronizer

- Two flexible reference inputs
- Automatic or manual holdover/switchover modes
- Digital PLL core
- Programmable loop bandwidth < 1 Hz
- Programmable output frequencies from 8 kHz to 750 MHz
- Differential HSTL clock output
- 64-lead LFCSP
- 5k price: $11.39

Increase performance and reliability while cutting your system clock costs by 50%

With Analog Devices' newest timing innovations, you can develop efficient and effective clocking designs.

The AD9516 combines low phase noise clock generation with 14-channel clock distribution at jitter levels below 500 fs. Integration of all critical timing functions into a single IC not only reduces costs and board space, but also diminishes reliability risks associated with large discrete oscillators. By implementing the AD9516, designers save time and reduce problems related to common debug issues when dealing with multiple components. Five versions with on-chip VCOs tuned from 1.45 GHz to 2.95 GHz make them ideal for communications and instrumentation designs.

The AD9549 is a two input clock generator with extended holdover functionality. It has a programmable loop filter to clean jitter from input clocks and features flexible output frequency options. In addition, automatic redundant reference clock switchover with user-selectable rate of phase adjustment helps ensure system stability and maximizes network uptime.

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